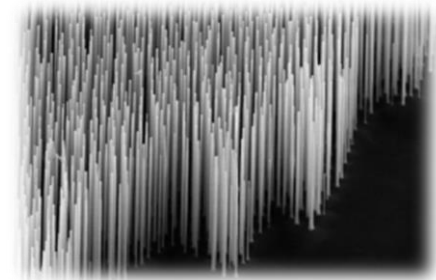


Heterogenous nucleation of catalyst-free InAs NWs on silicon

S. Battiato, V. Zannier, U. P. Gomes, D. Ercolani and L. Sorba

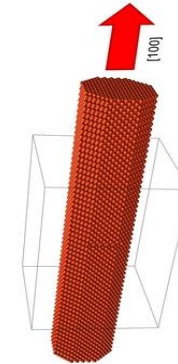
NEST, Istituto Nanoscienze-CNR and Scuola Normale Superiore, Piazza S. Silvestro 12, 56127
Pisa, Italy

- Introduction
- Semiconductor nanowires: definition and applications
- Catalyst-free growth of semiconductor NWs
- Nucleation and density control
- Conclusions



Semiconductor nanowires (NWs)

Quasi-one dimensional crystals

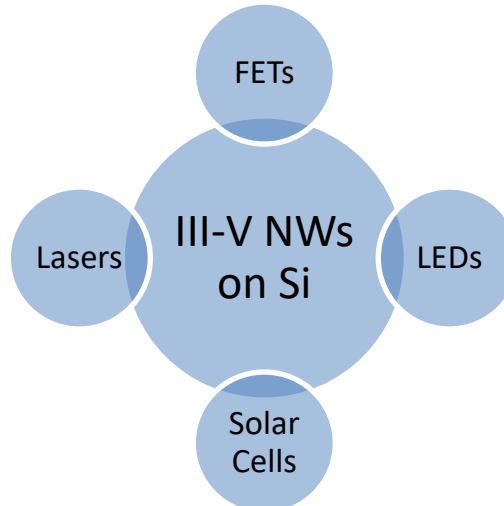


1D growth

➤ Novel properties:

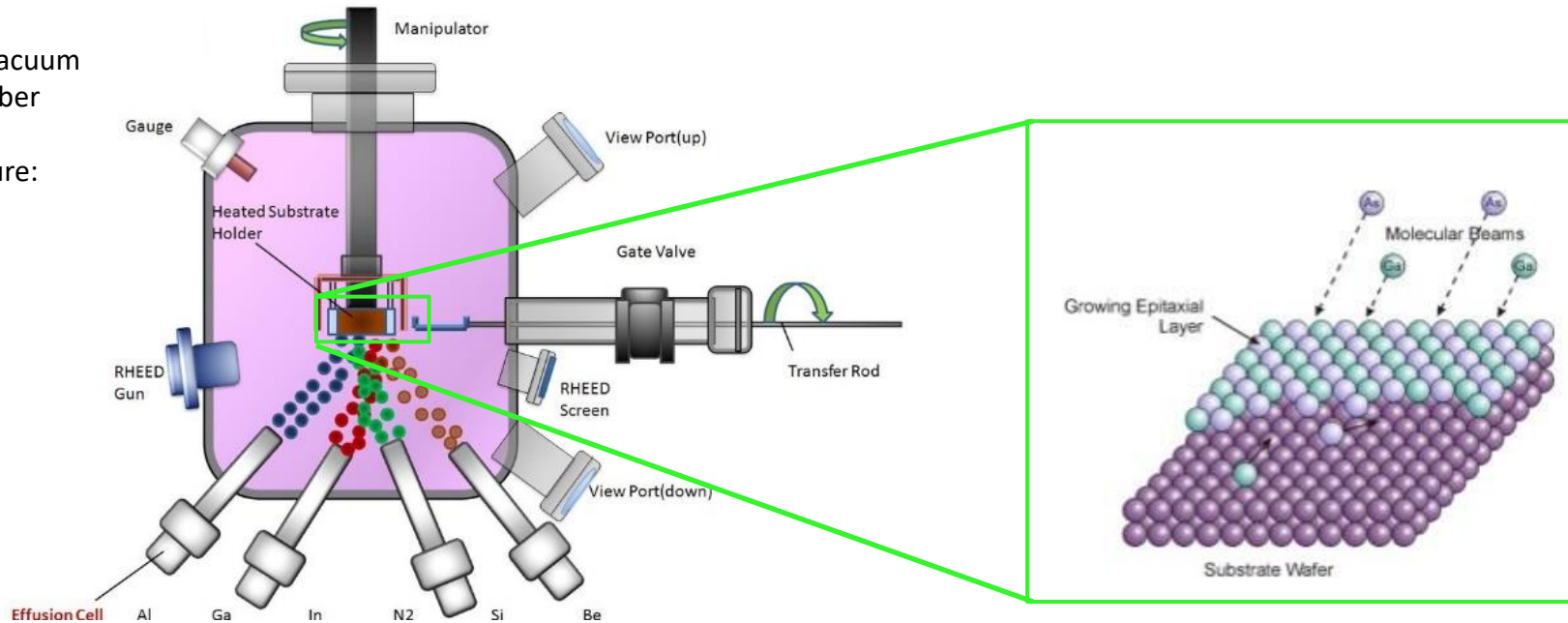
- high surface/volume ratio
- carrier confinement
- defect-free growth

➤ Integration of III-V NWs on silicon substrate → key research



Ultra High Vacuum (UHV) chamber

(base pressure: 10^{-9} Torr)



CBE for III-V semiconductor growth

Metal-organic precursors:

Group III : **TMIn**, TEGa, TMAI

Group V : **TBAs**, TBP, TDMASb, TMSb

n-doping: TBSe

Advantages +++

- Low impurity conc. thanks to the UHV
- Monolayer control
- High flexibility in material combination

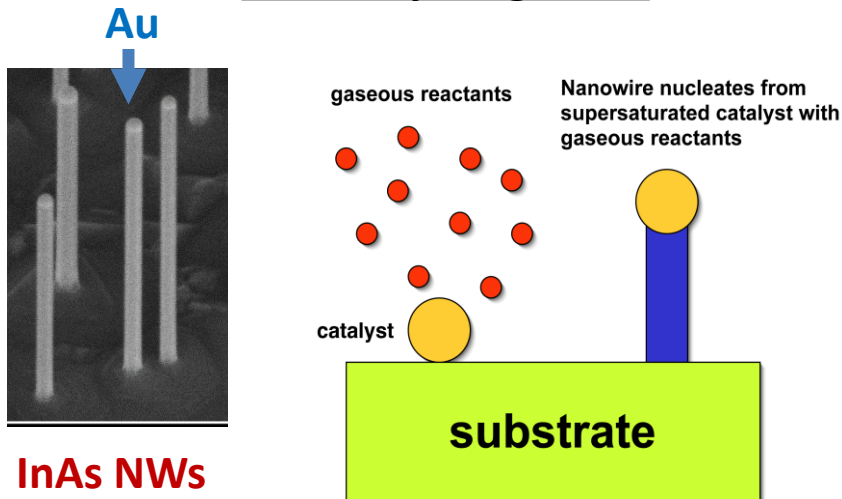
National Enterprise for nanoScience and nanoTechnology

Gold-assisted growth:

Vapour-liquid-solid (VLS) mechanism

Catalyst-free growth:

Au-catalyzed growth



Sputtering

NW growth



Substrate

InAs NWs



- ✓ Absence of foreign metal contamination
- ✓ Integration of III-V NWs and silicon



× The incorporation of metal catalyst can be detrimental for the applications



Catalyst-free growth of InAs NWs on Si (111) substrate

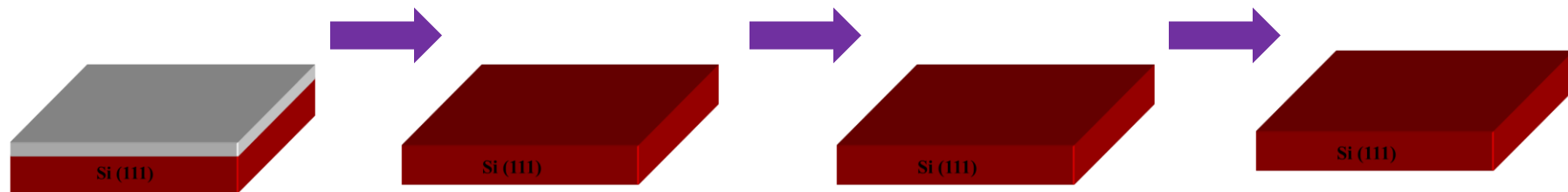
Substrate preparation



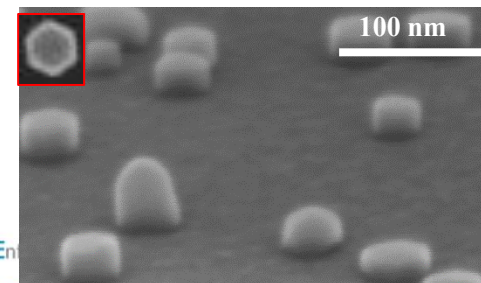
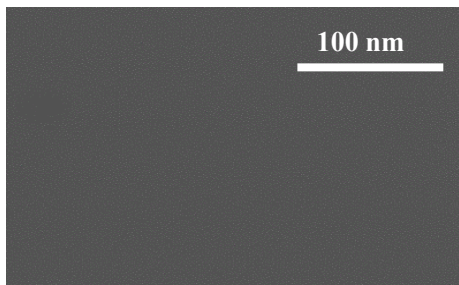
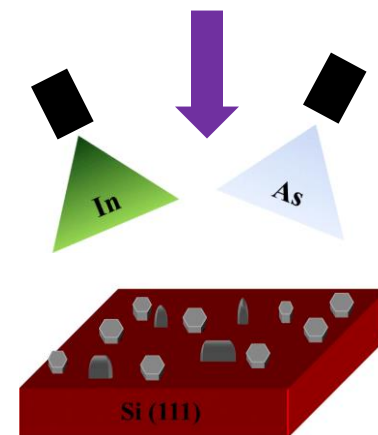
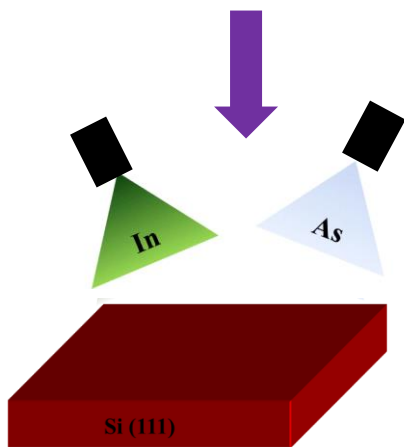
2' BOE etch + 10'' DI rinse + dry N₂

Ar or SiO₂ Sputtering

2' BOE etch + 10'' DI rinse + dry N₂



III-V Growth



National En

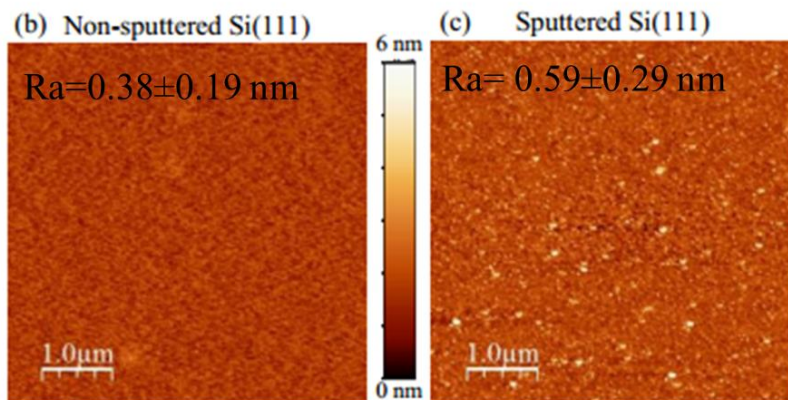


Nature of nucleation sites



HRTEM analysis shows defect-free crystalline order of the sputtered Silicon substrates after growth.

Amorphization is unlikely to be the source of nucleation sites.



AFM scans show comparable surface roughness.

Roughness is unlikely to be the source of nucleation sites

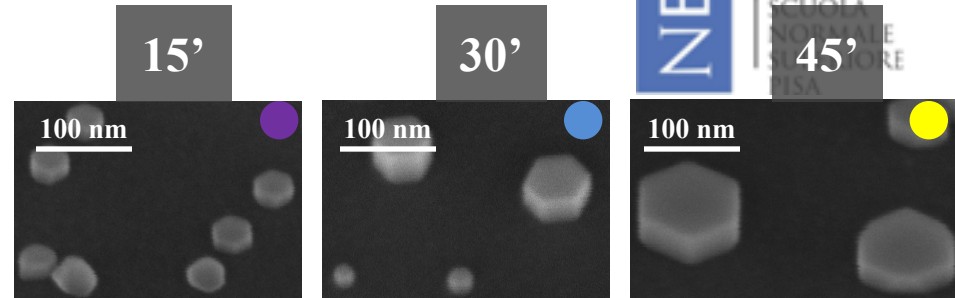
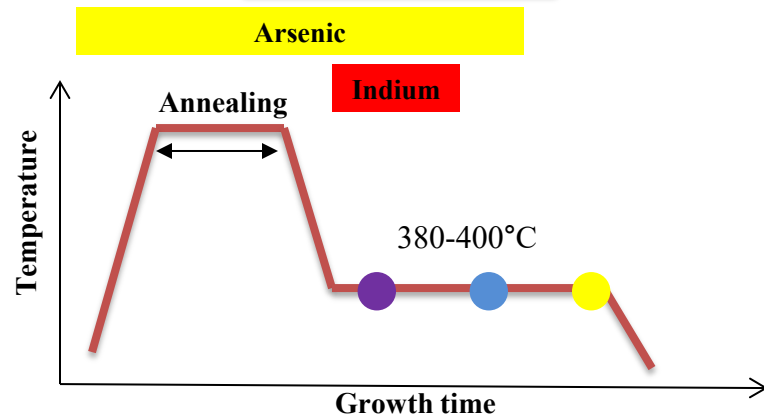
- ✓ Sputtering probably creates defects
- ✓ Defects serve as nucleation sites

Catalyst-free growth of InAs NWs on silicon substrate

Growth protocol

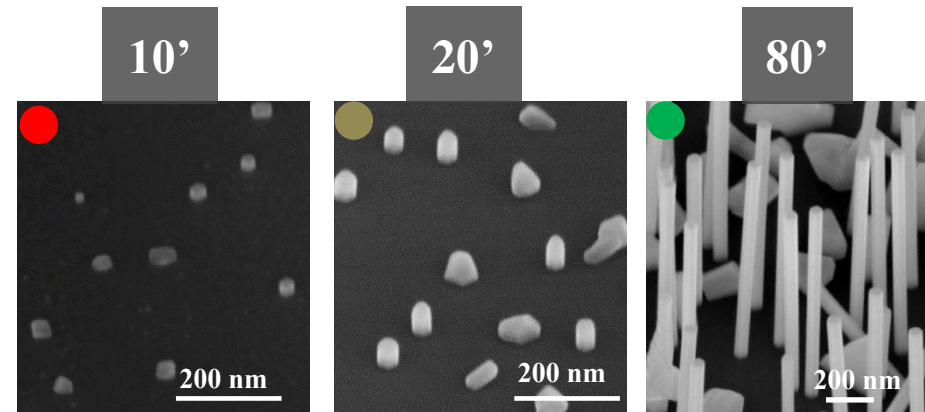
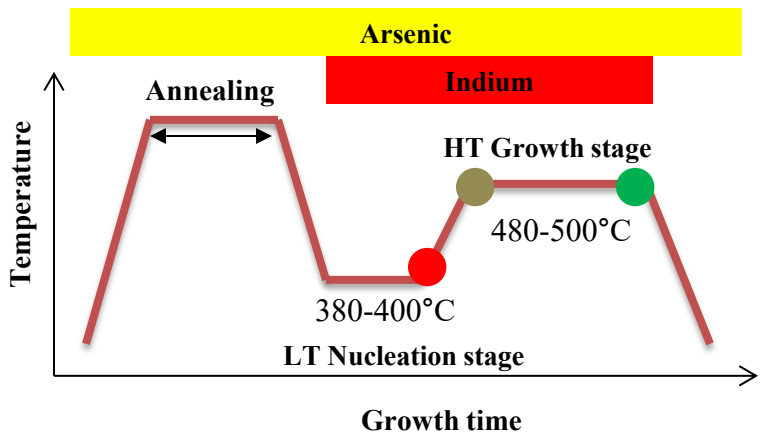


High Radial growth
Low axial growth



- LT enhances nucleation but NWs grow only radially

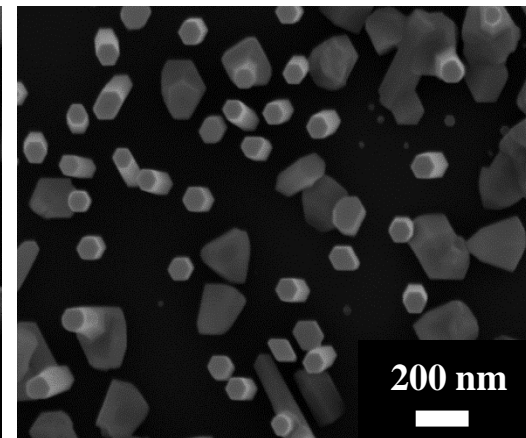
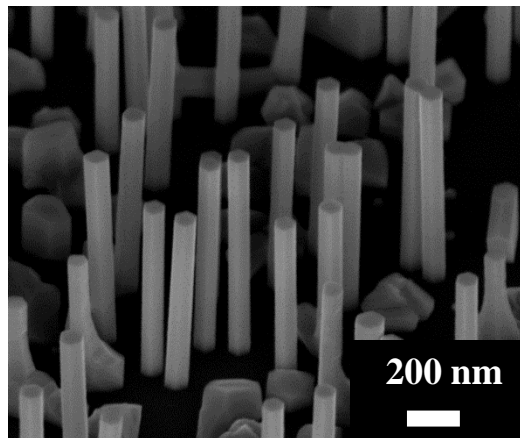
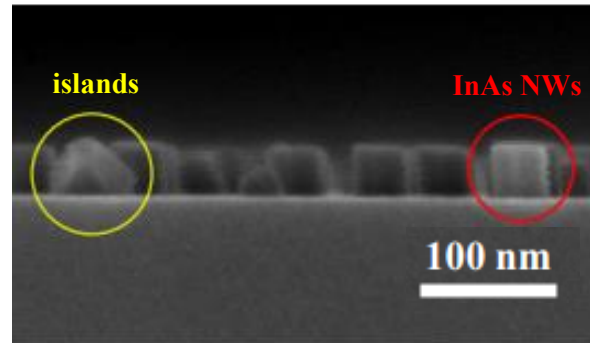
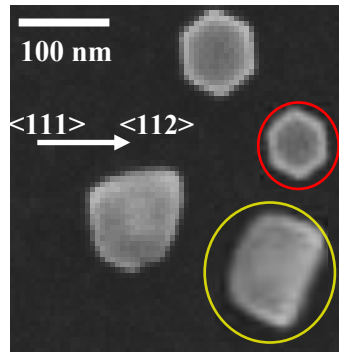
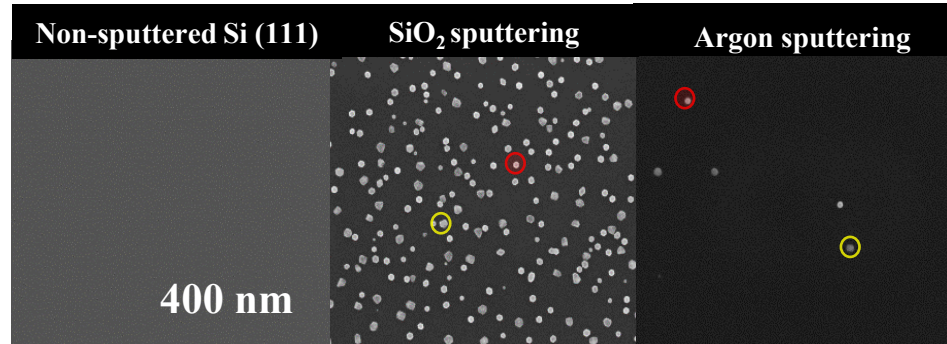
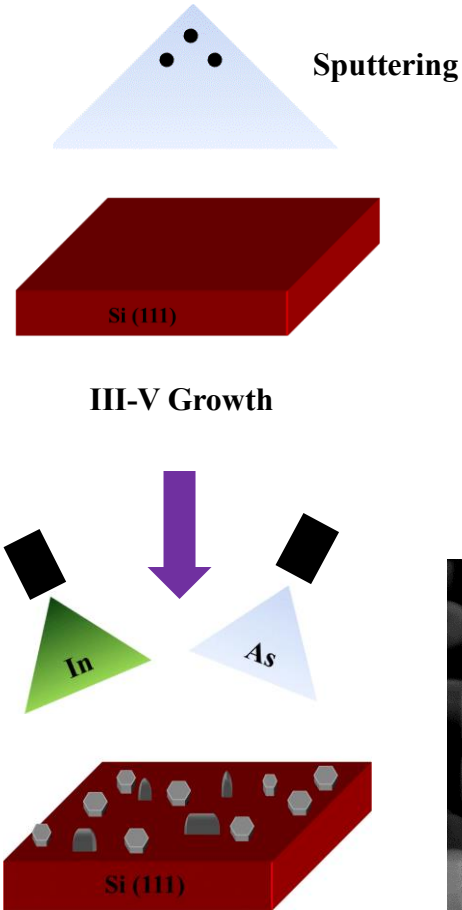
High Axial growth
Low radial growth



- Two-step temperature growth
- LT nucleation stage to control NW density
- HT stage to enhance anisotropic NW growth

Catalyst-free growth of InAs NWs on silicon substrate

Morphological characterization

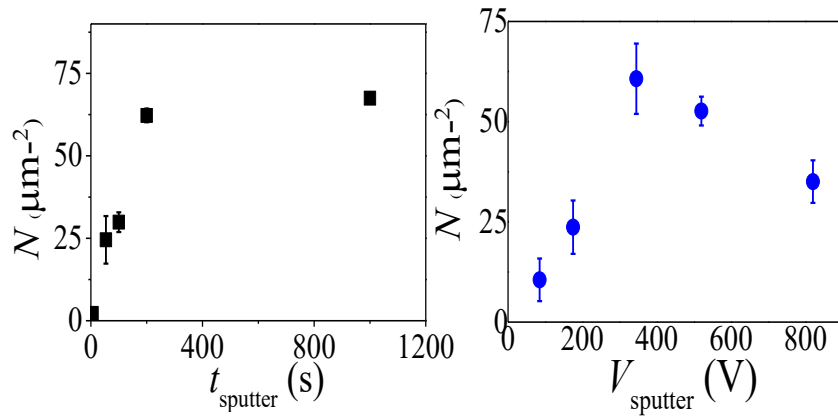
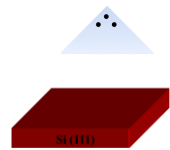


Catalyst-free and growth of InAs NWs on silicon substrate

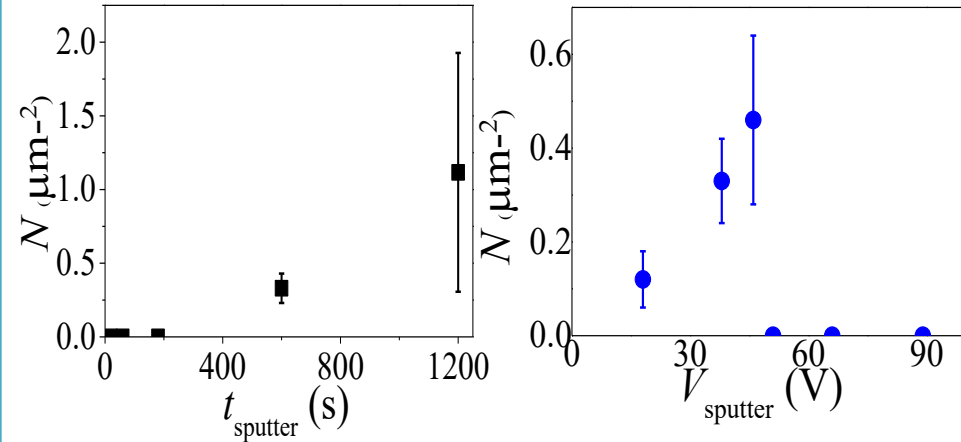
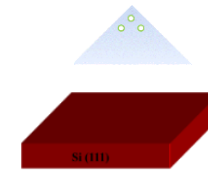
Effect of sputtering parameters on crystal density and yield

Crystal density (N) = (InAs NW + island) density
Yield = InAs NW density/total crystal density

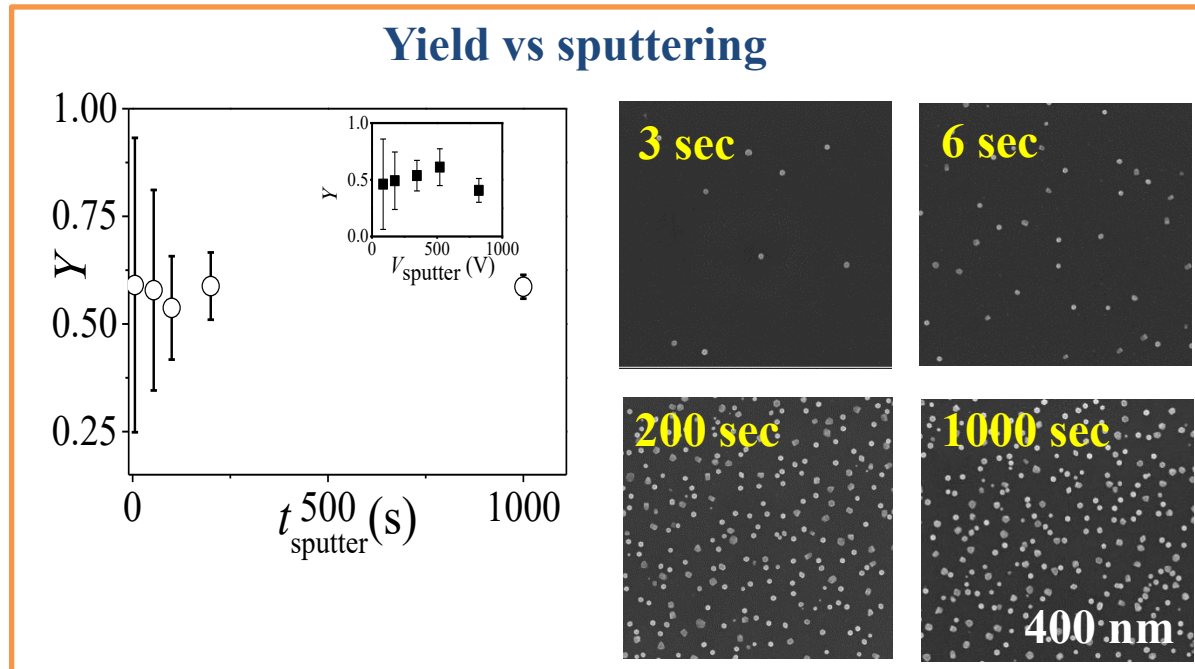
RF magnetron SiO₂ sputtering



ICP Argon sputtering



❖ Crystal density increases with t_{sputter} and V_{sputter} (as long as no etching /amorphization occurs)



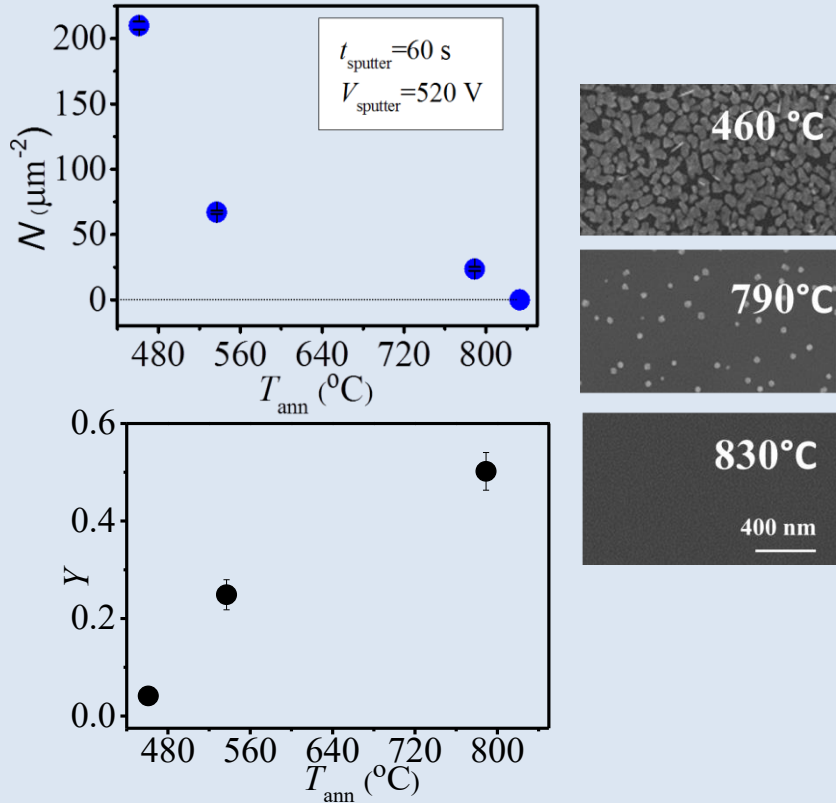
❖ Yield independent of sputtering parameters (~ 50 %)

Catalyst-free and growth of InAs NWs on silicon substrate

Effect of growth parameters on crystal density and yield

$T_{\text{annealing}}$

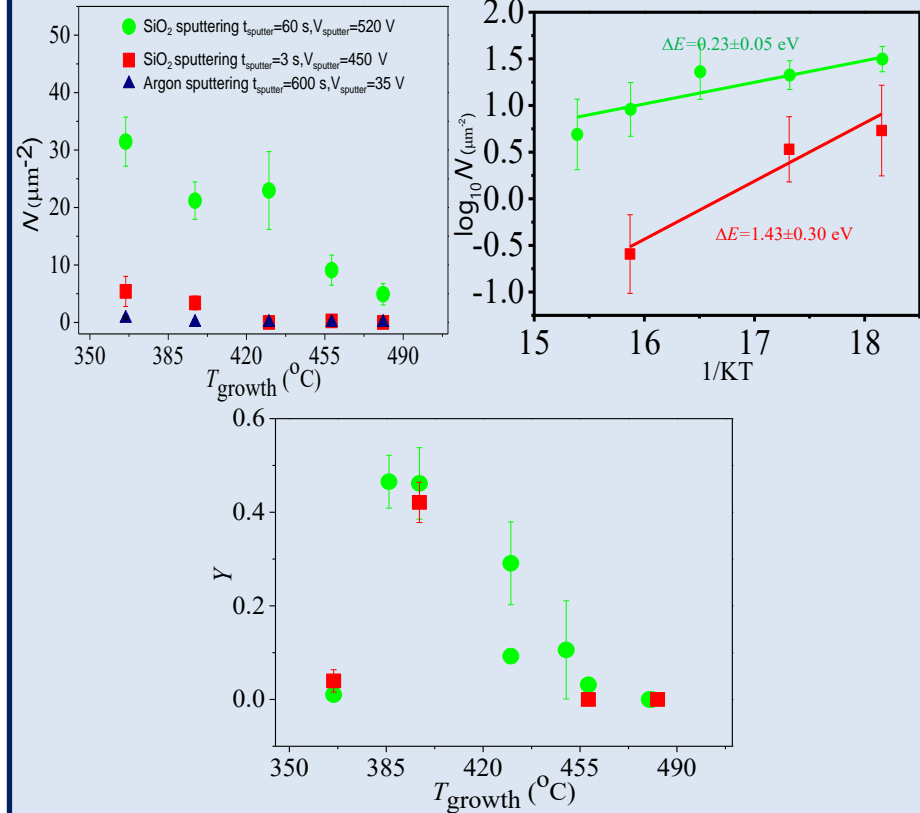
$T_{\text{growth}}=400^{\circ}\text{C}$, $t_{\text{growth}}=15$ mins



- Annealing reorders the Si (111) surface
- Defect density decreases and crystal density decreases
- Yield increases with annealing temperature
- Highest yield of NWs at 790 $^{\circ}\text{C}$

T_{growth}

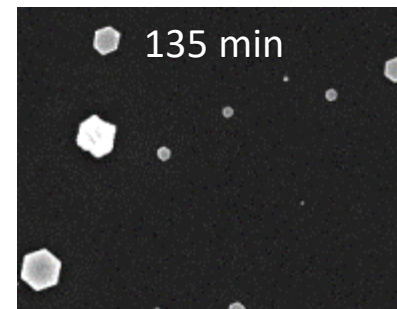
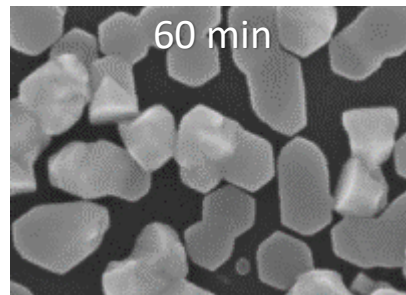
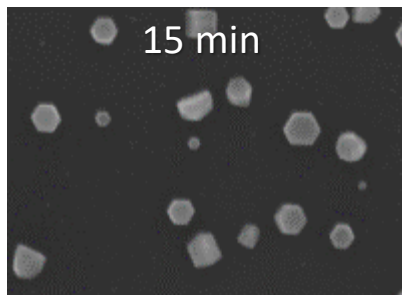
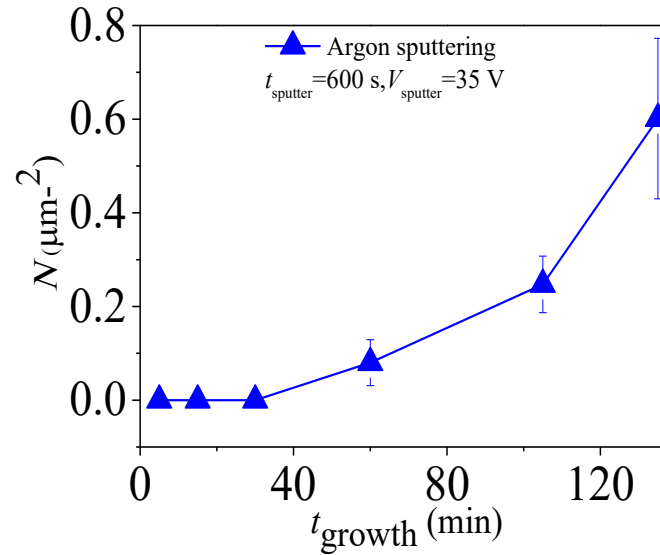
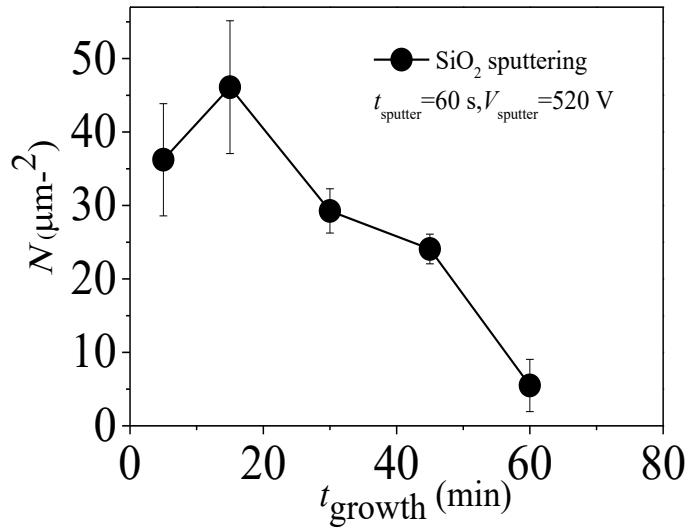
$T_{\text{ann}}=790^{\circ}\text{C}$, $t_{\text{growth}}=15$ mins



- InAs crystal density decreases with T_{growth}
- High V_{sputter} and t_{sputter} : low ΔE (Arrhenius plot)
- Maximum yield at 400 $^{\circ}\text{C}$
- NW density in the range of 1-30 NWs/ μm^2

Catalyst-free and growth of InAs NWs on silicon substrate

Nucleation mechanism vs growth time



Conclusions

- ✓ InAs nanowires have been synthesized by Chemical Beam Epitaxy technique using catalyst-free approach.
- ✓ We assessed the role of substrate preparation, showing that InAs crystals nucleate on sputtered Si(111) surfaces while no nucleation occurs on non-sputtered Si(111) surfaces.
- ✓ We showed that the silicon surface can be obtained by modifying in situ growth and ex situ sputtering parameters, allowing us to achieve a good control of the InAs NWs density.
- ✓ Although the nucleation of parasitic islands could not be completely inhibited, the yield of NWs could be increased to about 0.5 by proper choice of growth and annealing temperatures.

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Thank you for your attention!