A programmable metrological standard based on the quantum Hall effect

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Presentation outline

1) The quantum Hall effect & quantum Hall Metrology

- 2) Novel device
- 3) Experimental results
- 4) Numerical results
- 5) Conclusions and perspectives









$R_{K} = 25.81280755710 k\Omega$ Verified up to 11 decimal figures







Quantum Hall effect physics

Integer quantum Hall Cyclotron orbits quantization (Landau levels)





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Quantum Hall effect physics

 $\mu_R - \mu_L = (-e)V$

 $I = (-e) \int_{k_L}^{k_R} v_x(k) \frac{dk}{2\pi} = \frac{e^2}{2\pi\hbar} \int_{\mu_L}^{\mu_R} dE(k) = \frac{e^2}{h} V$

$$R_{1edge} = \frac{V}{I} = \frac{h}{e^2} = R_I$$

v = number of
propagating
1D modes at
the edges and
Landau levels
occupied.

ΘB







SQUID Wiring CCC Windings Null Detector Windings SQUID Socket SQUID PCB pick-up coil connection Null Detector Pick-up Coils

... Comparing currents with high precision

CCCs

Cryogenic Current Comparators

Advantages:

 Very high precision due to SQUID magnetometers

Problems:

- Intricate setup
- Incompatibility of measuring apparatus with the presence of a magnetic field

Separate insulated cryogenic setup for the SQUID



Overlapping Lead Shielding

... Generating new standards by scaling $R_{\ensuremath{\kappa}}$ value







Hall bar with multiple lateral shorts:

 Few fractions of R_K obtainable

... Are there other ways?

QHARS:

Quantum Hall Array Resistance Standard

Advantages:

 Conventional cryogenic setup

Problems:

- Presence of Ohmic resistances
- One standard per device
- Number of standards scales with the number of elements

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Building block

Two barriers in series



$$\begin{cases} x = \frac{V_b}{2} + \frac{z}{2} \\ y = z = \frac{w}{2} \end{cases}$$
$$y - x = -\frac{V_b}{3}$$

Various fractions generated without interconnected elements.



Recap and thesis targets

Open problems in resistive metrology

Solved with the scheme in the paper

Generating ad hoc standards on the same device

→ Stray resistances due to interconnected elements

Reduce the number of elements and increase number of standards

Double cryogenic setup for CCC

Open questions in the article studied How precise is the edge mixing ?

Large voltage bias or current is need to achieve high precision: What is the maximum voltage applicable to the new device ? (breakdown voltage)

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Lab and cryogenic setup

Other cryostat



Minimum temperature Around 300 mK

Device architecture





High mobility GaAs/AlGaAs/ wafer from Weizmann institute grown by Vladimir Y. Umansky

Aluminium gates (barriers)

1

AuGeNi Ohmic contacts

Measurement setup



One lockin to measure longitudinal voltage drops One lockin to measure diagonal or transversal voltage drops

- How good is the equilibration or mixing (zero voltage) ?
- Mixing breakdown voltage at finite gate bias?

Gates characterization



Pinch-off at -0.25 V (**barrier realization**) Slight hysteresis

Contacts working correcly

2DEG characterization: transport parameters



2DEG characterization: beating features



Gate14 mixer calibration. Correct control of v under the gate.



Gate14 single mixer operation verification



Gate14&12 diagonal voltage drops and diagonal 4w resistance (2D map)



Gate14&12 longitudinal voltage drops (2D map)



Alternative working scheme for the mixer



Gate14 mixer equilibration check





Quantum Hall breakdown

(max voltage bias applicable)



Max voltage bias values



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Study of external influences on the precision: R_{out}



 $\delta V = -iRout$

$(V + \delta V)/I = (Rload/(Rload + Rout))R$

R_{4w} 4-w real resistance 4-w ideal j resistance

R = b(2 - b)(RK/2) $R_{4w} = (bR_L/(R_K + (2-b)R_L))(R_K/2)$ $R_{out} = R_K/(2-b)$ Configuration dependent!

Numerical R_{out} results for the generic bisector



Modeling QH potential drops as a resistive network



Conclusions

Correct equilibration of the edge channels for the bisection scheme

- Breakdown voltage was found to be ~ 10 mV
- Limited and predictable Rout

What application ? Other 2D materials ?

Grazie per l'attenzione